Requirements

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Requirements

- More efficient circuit solver when dielectrics are included

- Reduce memory leaks on SpisNum

- More flexibility on circuit elements (e.g. surfaces non taken into account in the circuit or source surfaces with fixed potentials)

- Interconnectors treating:
  - Modelling electron current drawned by interconnectors without modelling IC geometrically (SPIS-Science)
  - Detailed study of IC -> analytical models for system simus

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Requirements

- Output of ion energy distribution (covered by SPIS-Science)

- CEX generation should be consistent with the « complex pusher »

- Work&think on current scalers to stabilize simulations during EP firing transional regime

- Implement elastic collisions volume interactor

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